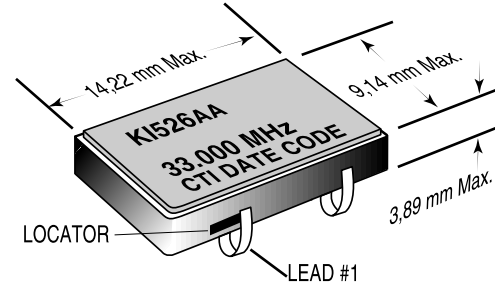**VCXO**

▲ **APPLICATIONS** Phase-Locked Loops (PLL's);
  Clock Recovery; Reference Signal Tracking; Synthesizers;
  Frequency Modulation/Demodulation
▲ Ceramic Construction, Metal Lid
▲ 2.0 to 55 MHz Frequency Range
▲ 0.5V to 4.5V Control Voltage
▲ ±25ppm Stability
▲ -40°C to 85°C Operating Temperature Option
▲ Tape and Reel Available
▲ Ground Shielded Top and Bottom
▲ 4-Pin SOJ-20 Footprint
▲ J-Leads
▲ Seam-sealed, Resistance Welded Hermetic Package

Application Specific Designs Also Available

K1526AA
33.000 MHz
CTI DATE CODE

14.22 mm Max.
9.14 mm Max.
3.89 mm Max.
LOCATOR
LEAD #1

MECHANICAL DIMENSIONS:
Refer to Appendix E-1 (Pg. 45),
Package Type: SMT Type A (VCXO)

ELECTRICAL SPECIFICATIONS

MODEL	K1526AA		K1526AD	
Frequency Range (MHz)	2 to 33	33.1 to 40	2 to 33	33 to 40
Frequency Stability (ppm)				
Overall	Inclusive of calibration, temperature, voltage, aging.			
0°C to 70°C	±25			±40
-40°C to +85°C	±50			±60
Frequency Control Function:	(for custom deviation range, Vc range, transfer function, etc.—consult factory)			
Deviation (ppm)				
Minimum	±100		±80	
Maximum	±150		±120	
Linearity	<5%	<10%	<5%	
Modulation Bandwidth (±3dB)	>20 KHz			
Nominal Control Voltage (V)	2.5			
Control Voltage Range (V)	0.5 to 4.5			
Transfer Function	Positive			
Input Impedance	>50 Kohms @ 10 KHz			
Temperature Range (°C)				
Operating	-40°C to +85°C			
Storage	-40°C to +125°C			
Supply Voltage (V)	+5.0 ±10%			
Input Current (mA)	<26	<50	<26	
Output	Refer to Appendix A-1, Table 1, Figure 1			
Symmetry (%) TTL	<33 MHz 45/55; ≥33 MHz 40/60			
Symmetry (%) CMOS	<33 MHz 45/55; ≥33 MHz 40/60			
Start Up Time (ms)	< 10			
Test Circuit Diagram	Refer to Appendix A-1, Figure 2			

Typical Phase Noise (dBc/Hz)		ORDERING INFORMATION
Offset from Carrier		
10 Hz	-65	
100 Hz	-95	
1 KHz	-120	
10 KHz	-140	
100 KHz	-150	

ORDERING INFORMATION

K1526AX X- Specify Frequency

"Blank" = 0°C to 70°C Operating Temp.
"M" = -40°C to 85°C Operating Temp.

Deviation options, see table above, or call CTI
for application specific requirements.

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